

Class	Subclass
ISSUE CLASSIFICATION	

## U.S. UTILITY Patent Application

SA	O.I.P.E.	PATENT DATE
SCANNED	KC3 Q.A. <i>Q.A.</i>	

APPLICATION NO. 09/837995	CONT/PRIOR D	CLASS 712	SUBCLASS 215	ART UNIT 2183	EXAMINER <i>Chen, E. J.</i>
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TITLE	APPLICANTS
1. <b>Chairman</b>	1. <b>Mr. J. Edgar Hoover</b>
2. <b>Vice Chairman</b>	2. <b>Mr. J. Edgar Hoover</b>
3. <b>Members</b>	3. <b>Mr. J. Edgar Hoover</b>
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Shubhendu Mishra jee  
Steven Reinhardt

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Simultaneous and redundantly threaded processor store instruction  
comparator

PTO-2040  
12/89**ISSUING CLASSIFICATION**

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

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	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
Amount Due			Date Paid	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	
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